ISQED Announces Multiple Interactive Tutorials

Nine Tutorials Explore Critical Areas of Low Power Design, Mixed-Signal Verification, TSV and Field Solver Solutions

SAN JOSE, CA – Jan. 25, 2010 -- The 11th International Symposium on Quality Electronic Design (ISQED2010) conference will feature nine interactive tutorials covering challenging design issues.

The conference will take place March 22-24, 2010, at the Doubletree Hotel, San Jose, Calif.

The list of ISQED tutorials includes:

Beyond 32 nm Technology
K. Maitra - Global Foundries

Energy Efficient Digital Circuit Design
Benton Calhoun - University of Virginia

Alternate Memories
Stuart Parkin - IBM

SOC Verification
Ken Albin - AMD

Paving the Road for 3D Through-Silicon Via (TSV) Design
Pol Marchal - IMEC

Microprocessor Architecture Impacts on Power
Dave Ayers – Intel

A Scalable Methodology for Analog & Mixed-Signal Verification
Shyam Rapaka, Tapan Halder - Synopsys

Field Solver Solutions for IC level design
Maxim Ershov, Silicon Frontline

Field Solver Solutions for System Level & RF
Dipanjan Gope, Swagato Chakraborty - Physware
Vikram Jandhyala - University of Washington, Seattle

In addition ISQED2010 features 7 keynote speeches by industry leaders, 23 technical sessions with nearly 150 papers in three parallel tracks, panel discussions and vendor exhibits.

ISQED2010 recipients of the prestigious IQ-Award, and best paper awards will be announced during the luncheon ceremony, sponsored by Synopsys, on Tuesday, March 23rd. ISQED Quality Award is sponsored by Mentor Graphics. ISQED best paper awards are sponsored by Synopsys.

ISQED2010 media sponsors are EDACafe and Chip Design magazine. The conference is held with technical sponsorship of the IEEE Electron Devices Society and IEEE CASS, and in cooperation with IEEE Reliability Society, and ACM/SigDA. ISQED is produced and sponsored by the International Society for Quality Electronic Design. For more information regarding the tutorials, conference, vendor exhibitions, workshops, and hotel registration visit http://www.isqed.org.

About the International Symposium on Quality Electronic Design

The International Symposium on Quality Electronic Design (ISQED) is a premier Design & Design Automation conference, aimed at bridging the gap between and integration of, electronic design tools and processes, integrated circuit technologies, processes and manufacturing, to achieve design quality. ISQED is the pioneer and leading conference dealing with design for manufacturability and quality issues, front to back. The conference provides a forum to present and exchange ideas and to promote research, development, and application of design techniques and methods, design processes, and EDA design methodologies and tools that address issues which impact the quality of the realization of designs into physical integrated circuits. ISQED's goal is to accelerate cooperation among the IC Design, EDA, and semiconductor process technology and manufacturing communities. Further information about ISQED can be found at http://www.isqed.org.

The conference is produced and sponsored by the International Society for Quality Electronic Design. For more information please visit: www.isqed.org.

-end-

Editorial Contact
Georgia Marszalek
ValleyPR for ISQED
Georgia@ValleyPR.com
+1.650.345.7477